Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet	1	of	2
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		Complete if Known
	Application Number	10/749.262
	Filing Date	December 31, 2003
	First Named Inventor	Jin-Tae Oh
	Art Unit	2434
_	Examiner Name	Powers, William S
	Attorney Docket Number	

	U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.1	Document Number Number - Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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	Foreign Patent Documents						
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		KR 2002-0072618 A	09-18-2002	Inst Info Tech Assessment		Ab	strad

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Complete if Known Substitute for form 1449A/PTO Application Number 10/749,262 **INFORMATION DISCLOSURE** December 31, 2003 Filing Date STATEMENT BY APPLICANT First Named Inventor Jin-Tae Oh Art Unit 2434 **Examiner Name** Powers, William S 2 Sheet 2 of Attorney Docket Number 3364P161

	NON PATENT LITERATURE DOCUMENTS				
Examiner Initiale*	Cite	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or	T²		
		Cho, Y.H., et al., "Specialized Hardware for Deep Network Packet Filtering," 12th Conference on Field Programmable Logic and Applications, Montpellier, France, Sep. 2002, pp. 452-461.			
		Sidhu, R.P.S., et al., "String Matching on Multicontext FPGAs using Self-Reconfiguration," Proceedings, 1999 ACM/SIGDA 7th Intl. Symposium on Field Programmable Gate Arrays, Feb. 1999, pp. 217-226.			

Examiner	Date
Signature	Considered

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